

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. SRINIVASAN et al. Examiner: Mark A. Radtke
Serial No. 10/647,997 Group Art Unit 2165
Filed August 25, 2003 Docket No. SVL920030042US1
TITLE METHOD, SYSTEM, AND ARTICLE OF MANUFACTURE FOR
PARALLEL PROCESSING AND SERIAL LOADING OF
HIERARCHICAL DATA

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being transmitted through the USPTO EFS-Web system over the Internet to Mark A. Radtke of the U.S. Patent and Trademark Office on November 29, 2006.

/Janaki K. Davda/
Janaki K. Davda

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Examiner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2.
Remarks/Arguments begin on page 10.